

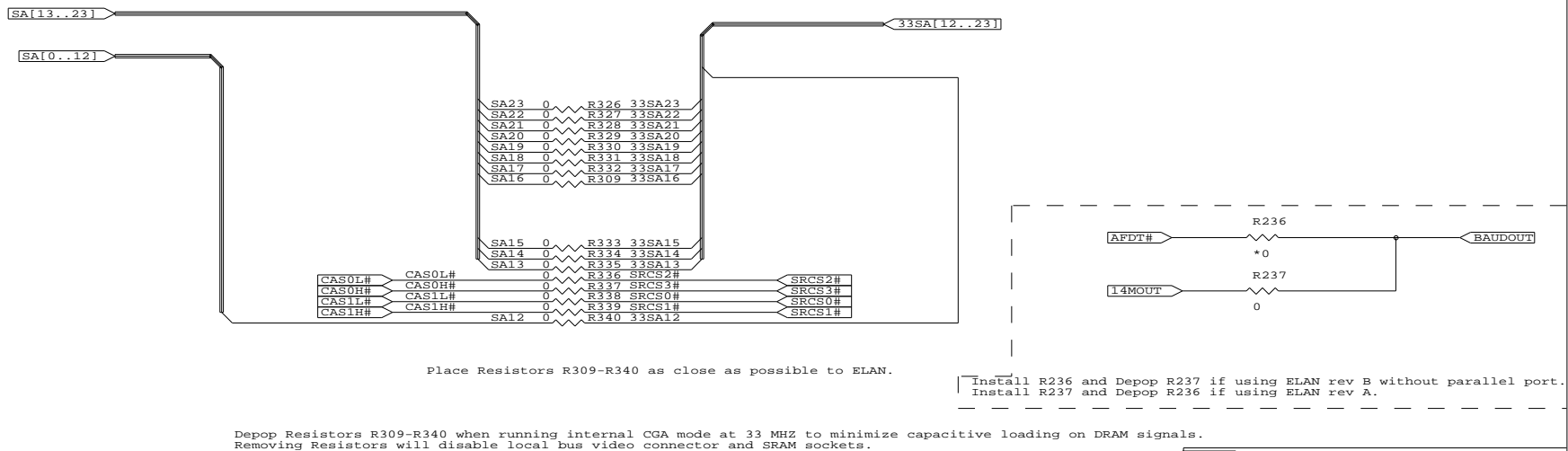
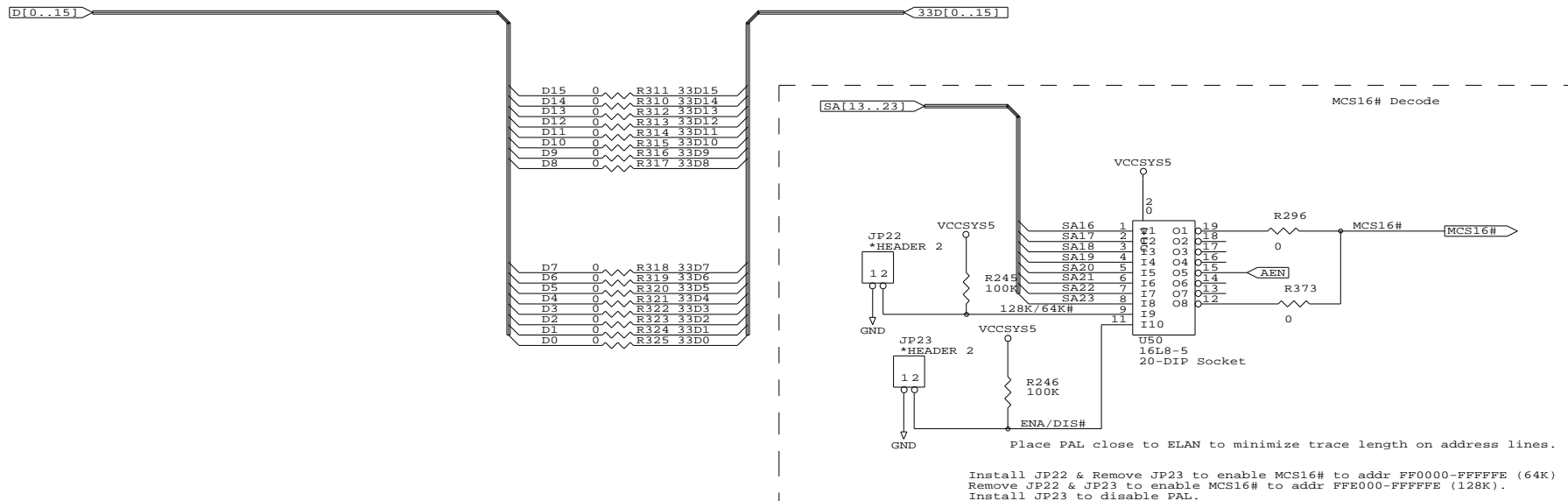


Disclaimer

These circuit diagrams are included for your interest only, and are not supported by AMD.

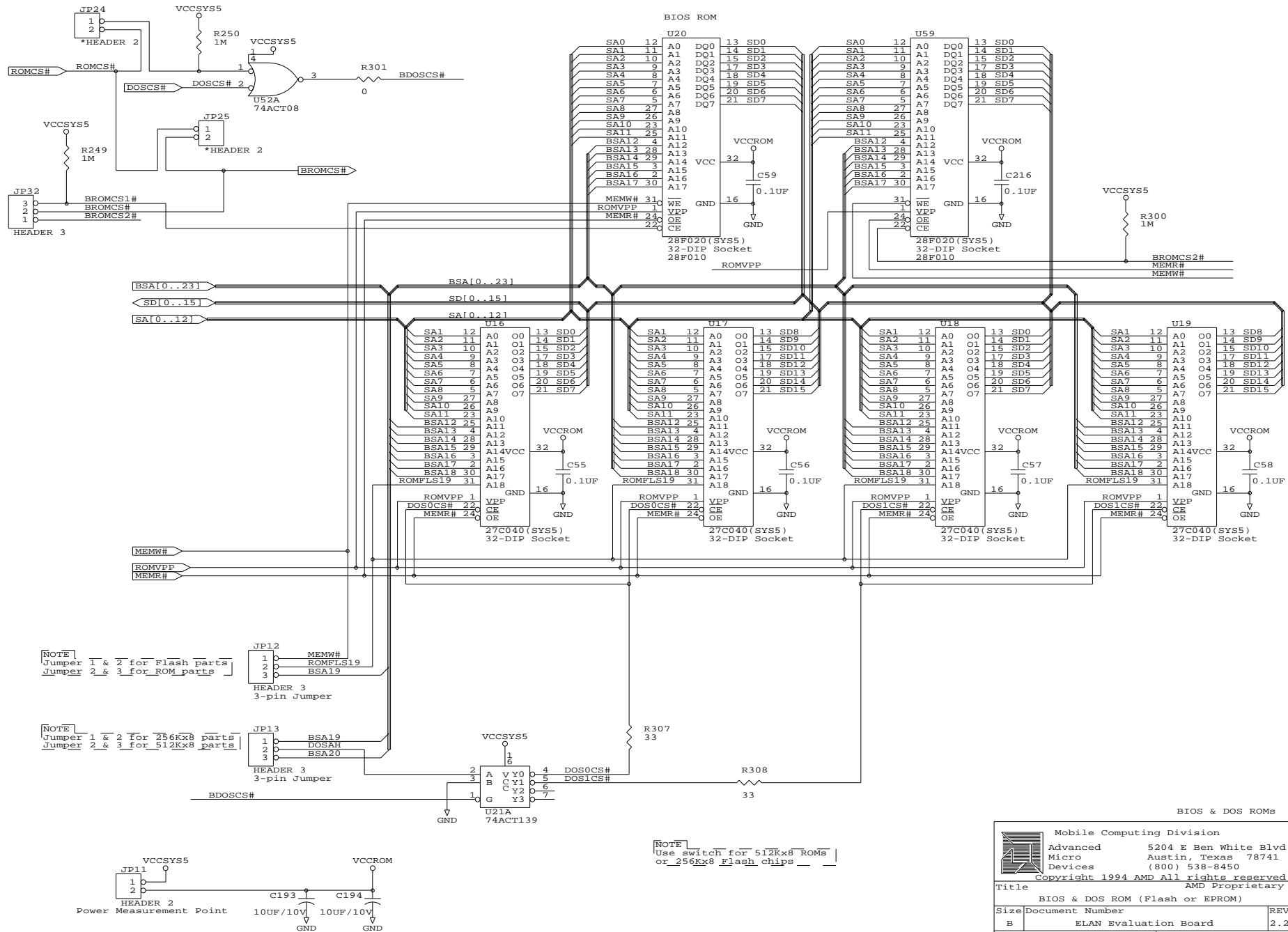
The correctness or otherwise of the information in these diagrams must be verified by the you the user.

Continue



Install JP24 and remove JP25 to enable BDOSCS# as the chip select for ROM BIOS accesses.
Remove JP24 and Install JP25 to enable BROMCS# during ROM BIOS accesses.

NOTE: Can place 128Kx8 Flash (28F010) in these sockets. A17 will not be used.



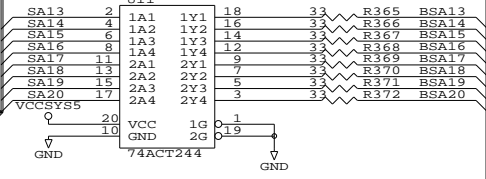
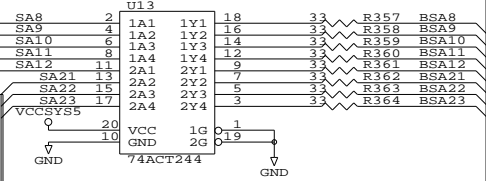
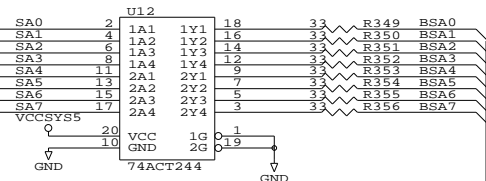
SA[0..12] SA[0..12]

VCCSYS5
C178
0.1UF
GND

VCCSYS5
C183
0.1UF
GND

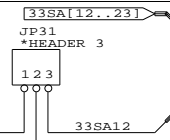
VCCSYS5
C179
0.1UF
GND

SA[13..23] SA[13..23]



System Address Bus Buffers

LVDD#

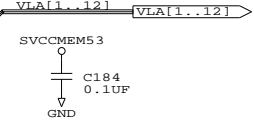
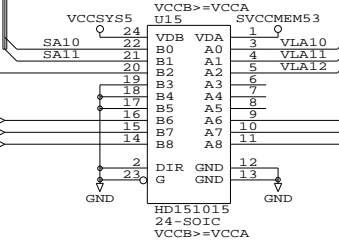
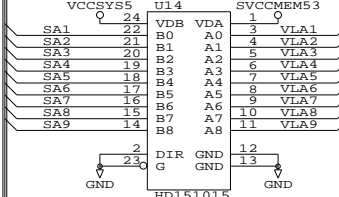


Install JP31 on pins 1&2 for ELAN rev B.
Install JP31 on pins 2&3 for ELAN rev A.

VCCSYS5
C180
0.1UF
GND

VCCSYS5
C181
0.1UF
GND

VCCSYS5
C181
0.1UF
GND



VCCSYS5
C182
0.1UF
GND

Local Bus Address Translating Buffers

SD[0..15] SD[0..15]

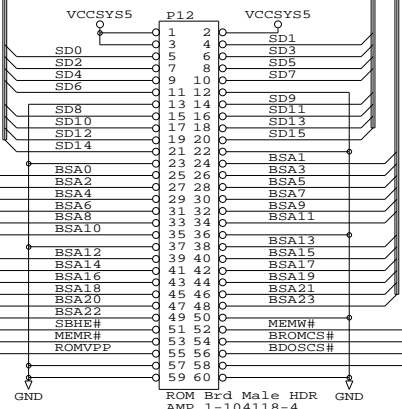
BSA[0..23] BSA[0..23]

Connector

1	00	2
3	00	4
5	00	6
7	00	8
9	00	10
11	00	12
13	00	14
15	00	16
17	00	18
19	00	20
21	00	22
23	00	24
25	00	26
27	00	28
29	00	30
31	00	32
33	00	34
35	00	36
37	00	38
39	00	40
41	00	42
43	00	44
45	00	46
47	00	48
49	00	50
51	00	52
53	00	54
55	00	56
57	00	58
59	00	60

Component side
of board


SBHE#
MEMR#
ROMVPP



MEMW#
BROMCS#
BDOSCS#
MCS16#

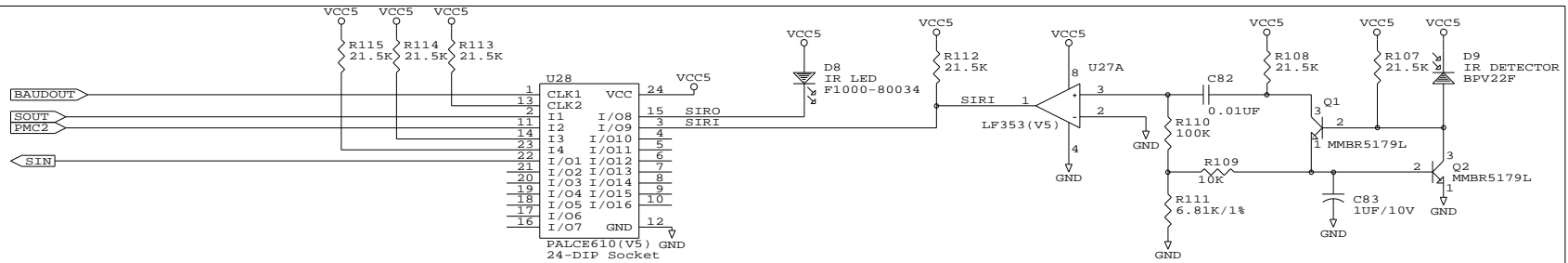
ROM Card Connector

[NOTE]
ROM Daughter card does full decoding
Depopulate ROMs on this board when using.

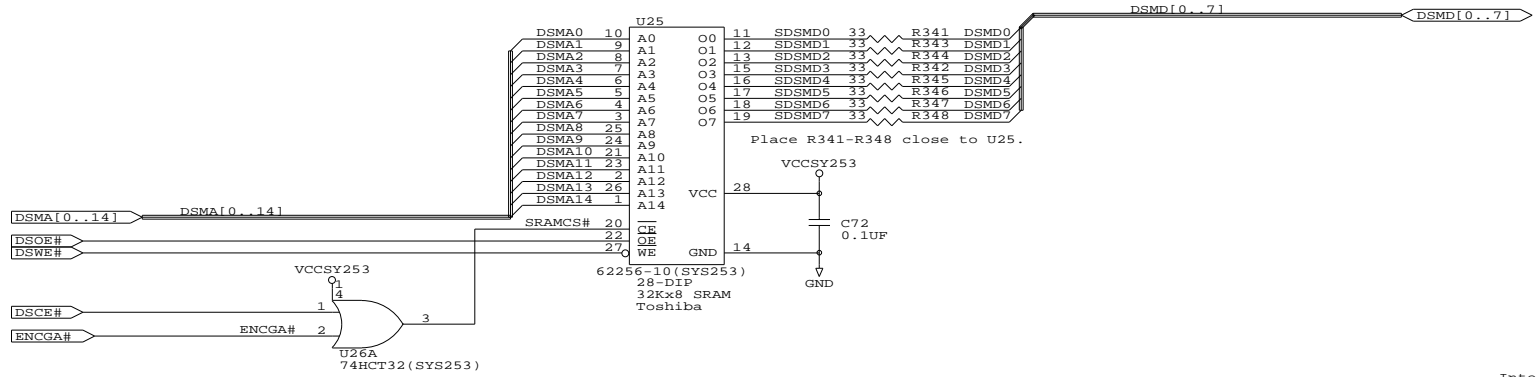


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Copyright 1994 AMD All rights reserved
Title AMD Proprietary
Address Buffering & ROM Connector
Size/Document Number
B ELAN Evaluation Board
Date: April 17, 1995Sheet 9 of 23

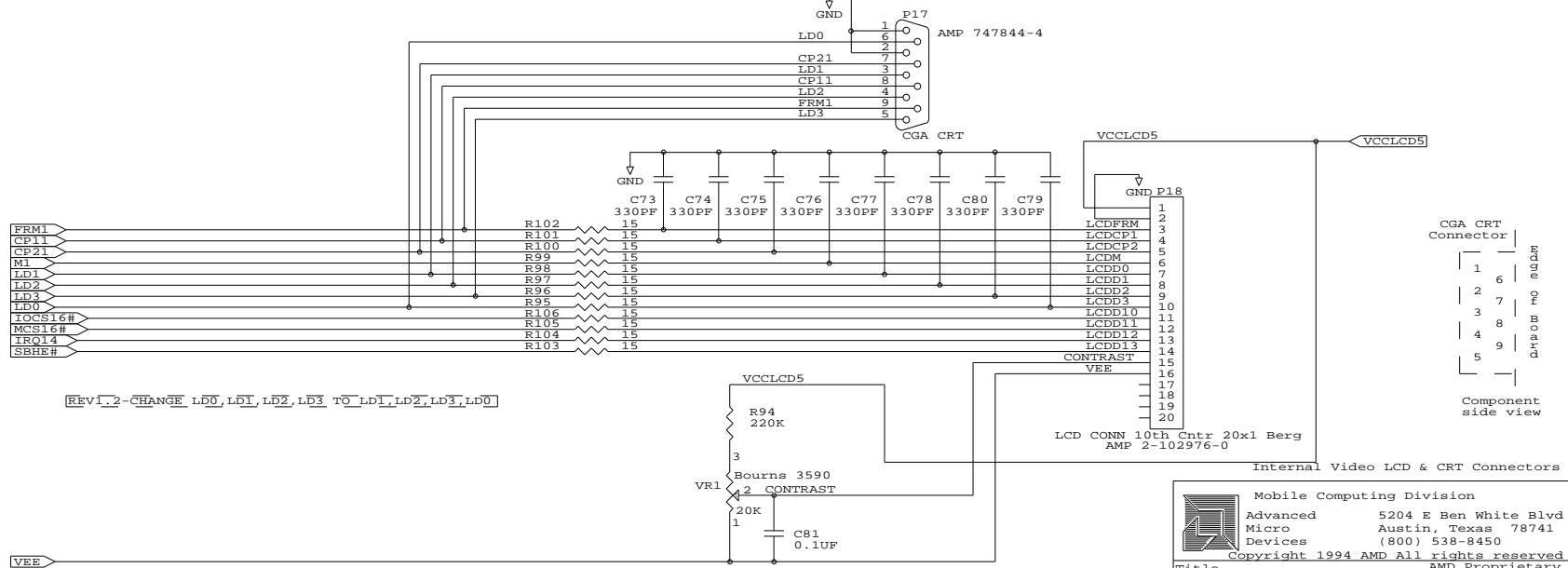
REV	2.2
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Serial Infra-Red Circuit

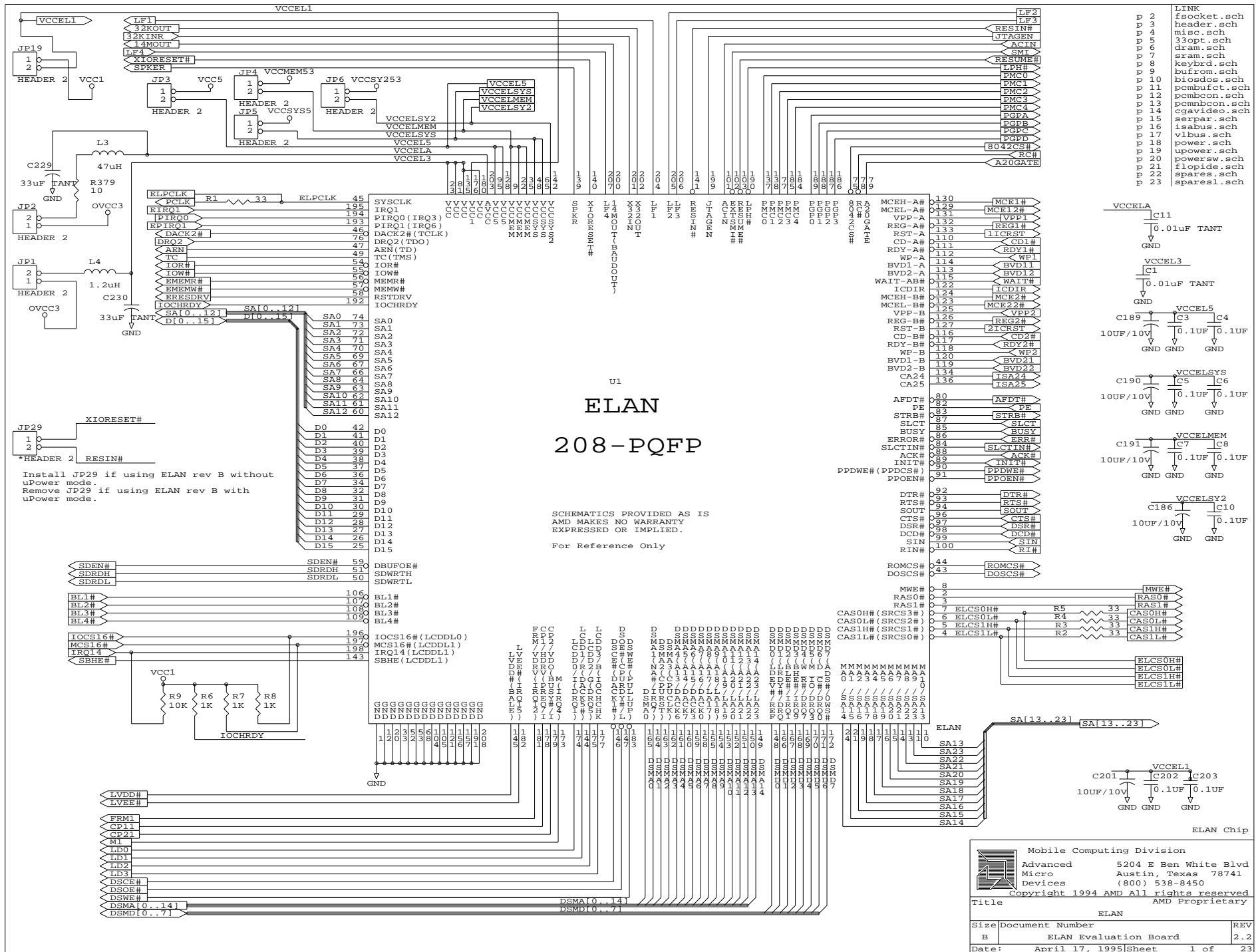


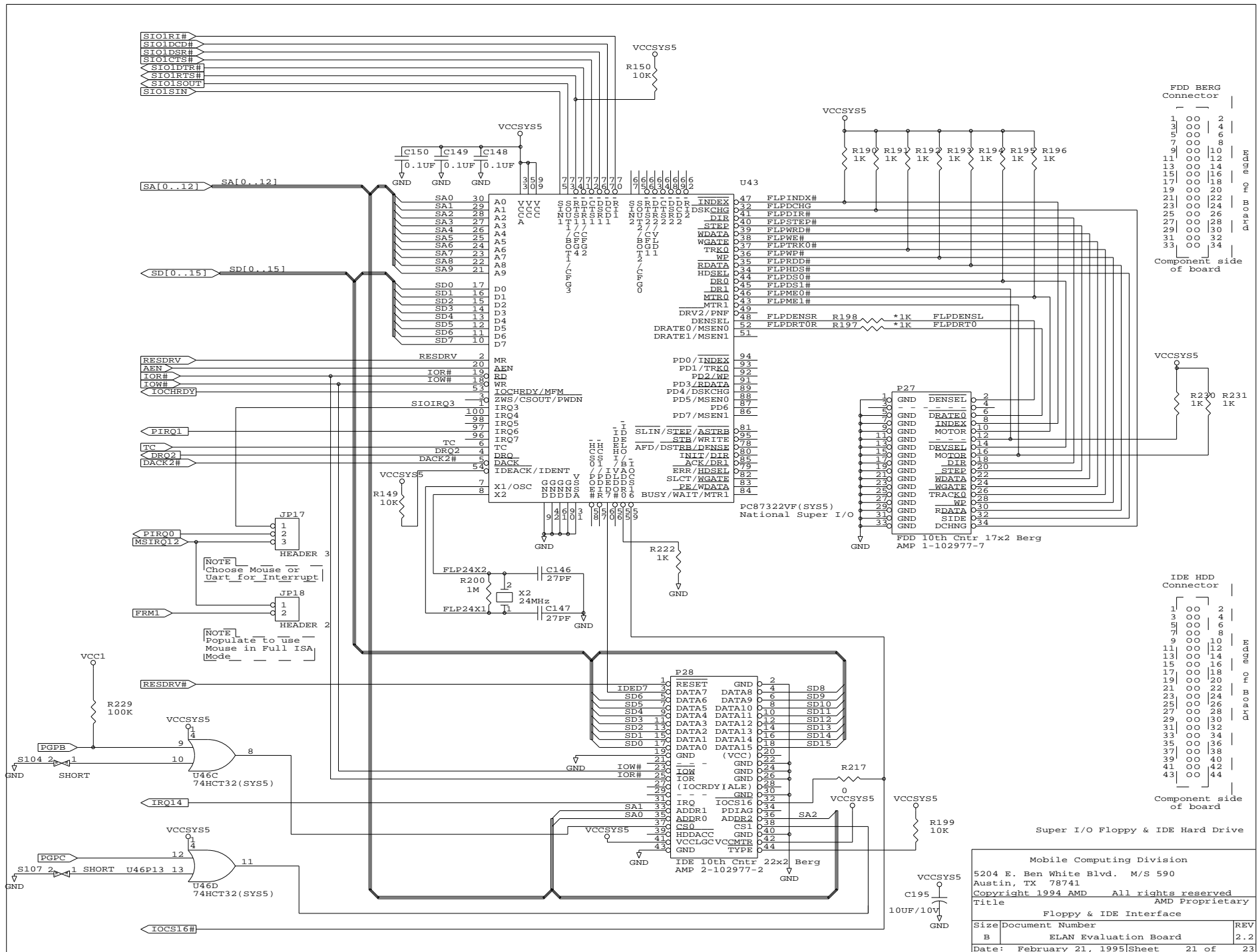
Internal Video RAM

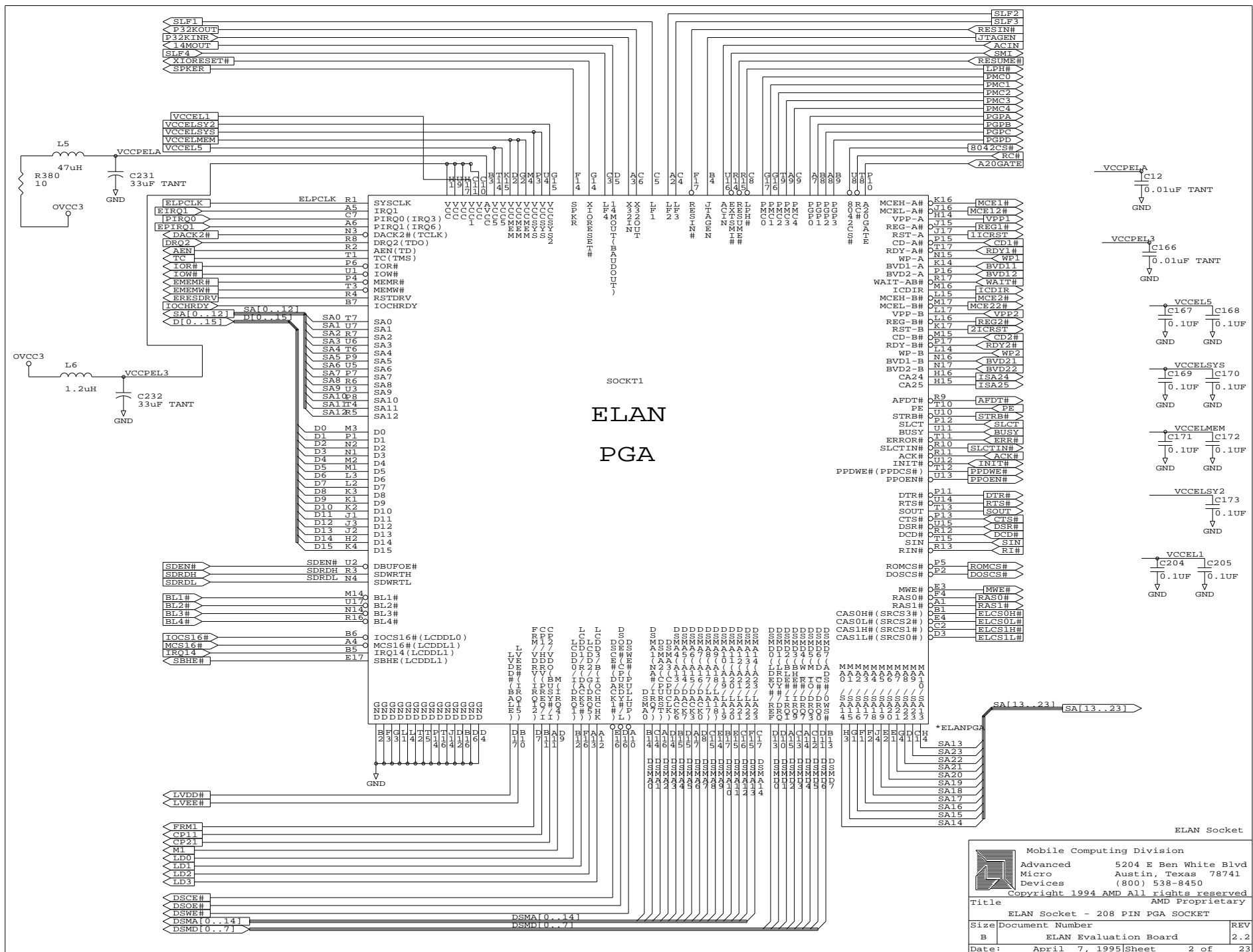


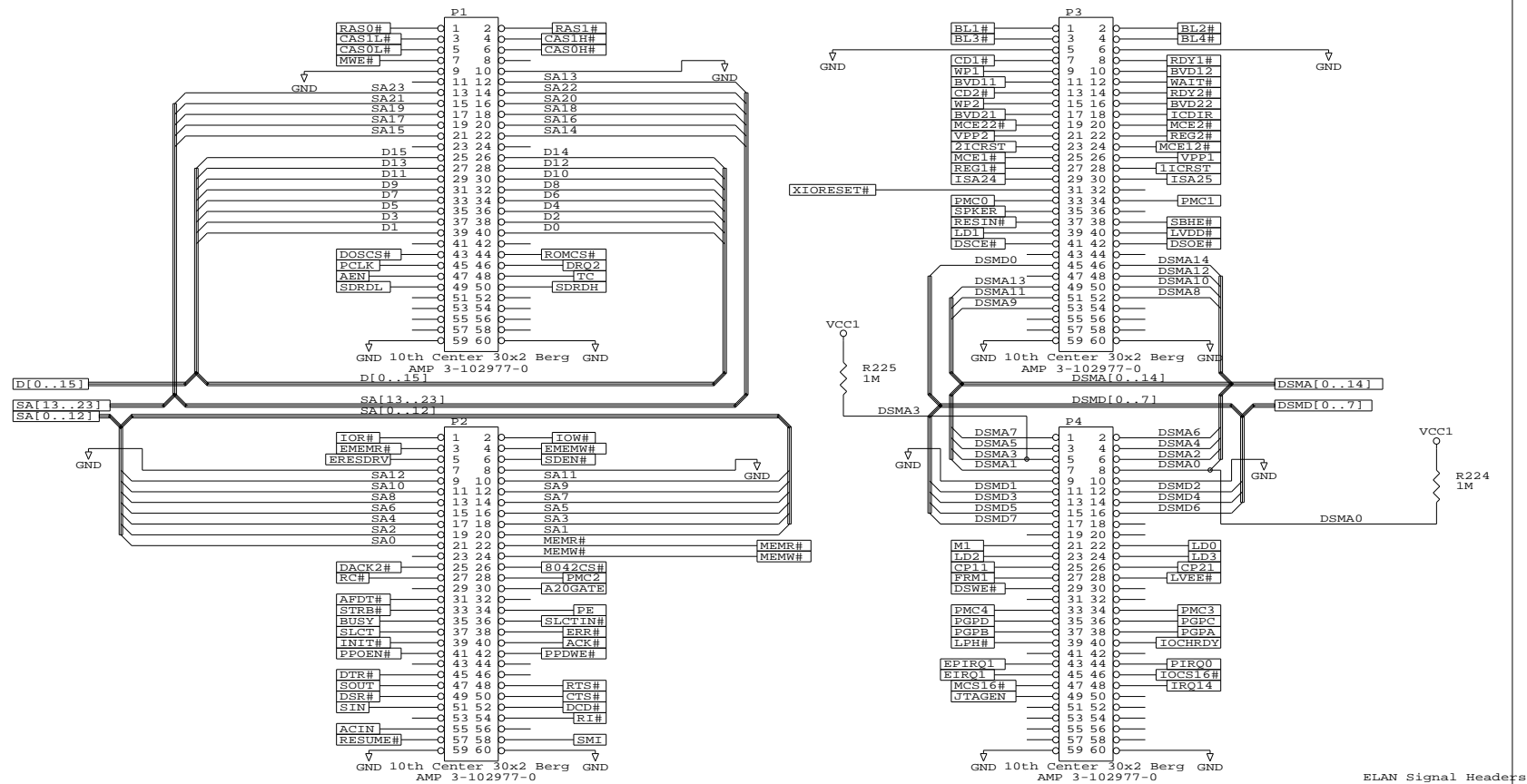
Internal Video LCD & CRT Connectors

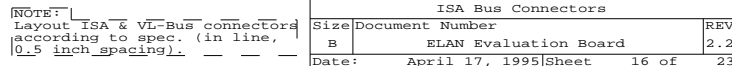
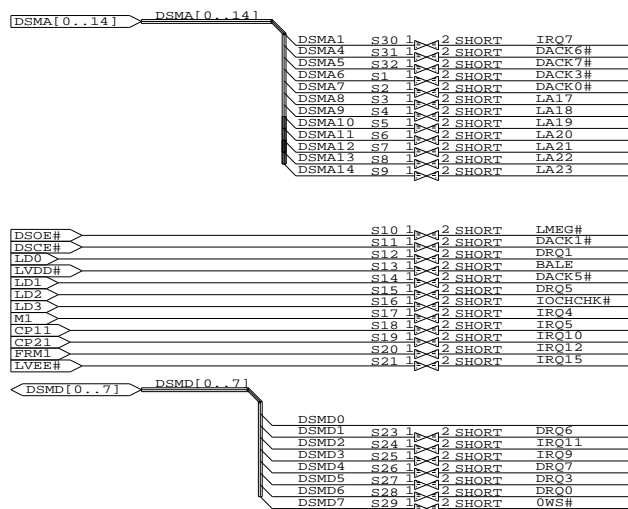
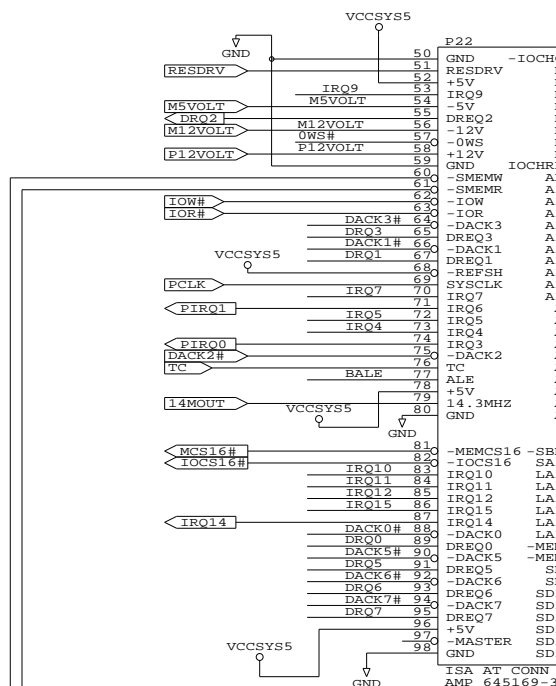
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Micro	Austin, Texas 78741	
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AMD Proprietary		
Title		
Display SRAM & CRT & LCD Connectors		
Size	Document Number	REV
B	ELAN Evaluation Board	2.2
Date:	February 21, 1995	Sheet 14 of 23

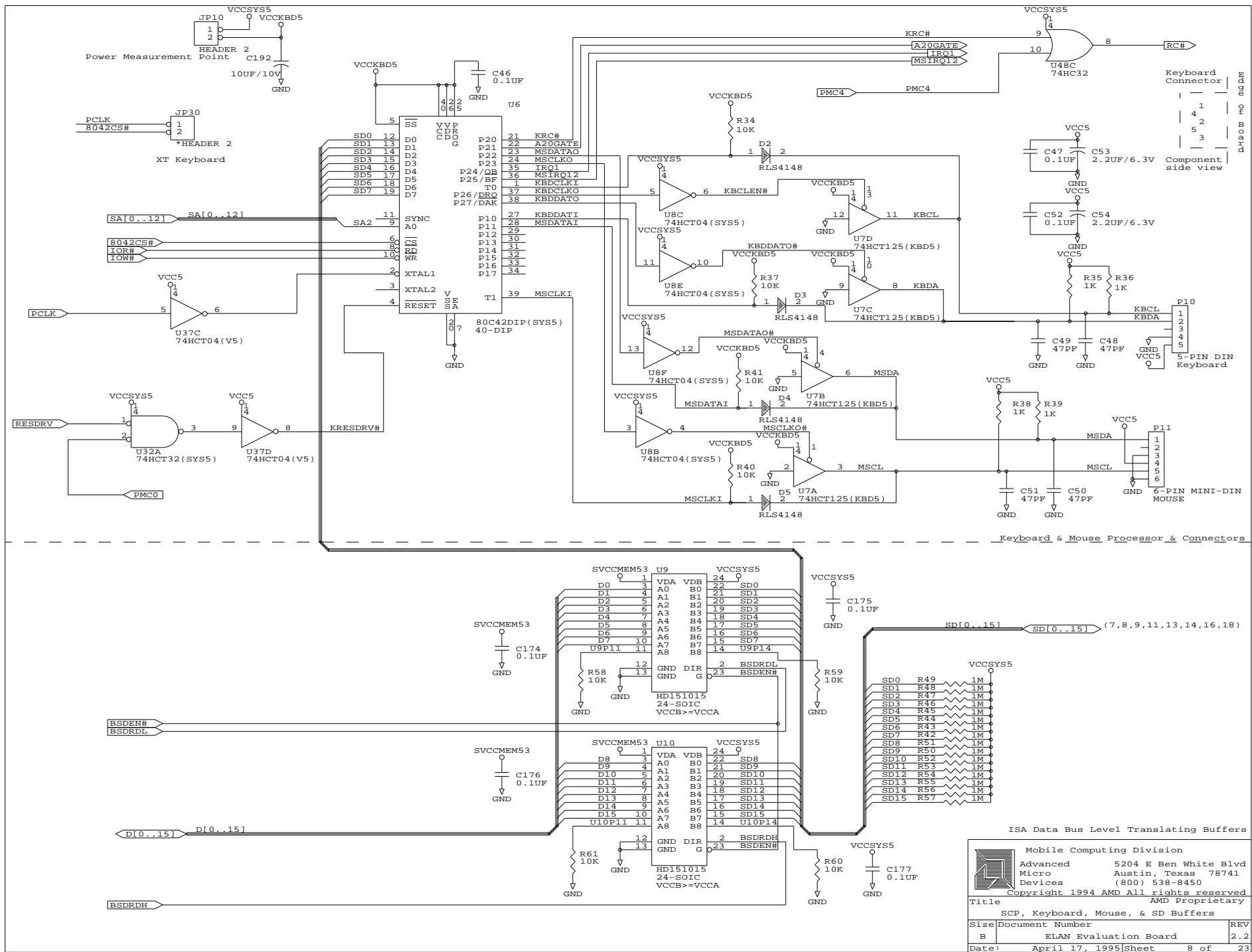


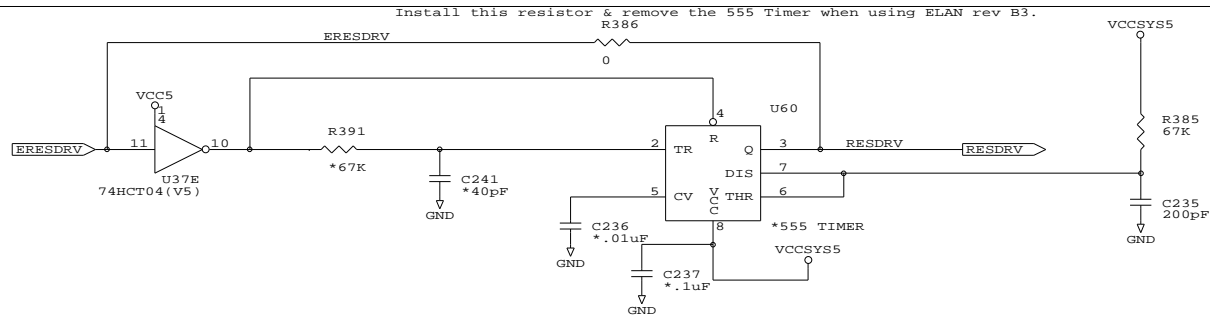




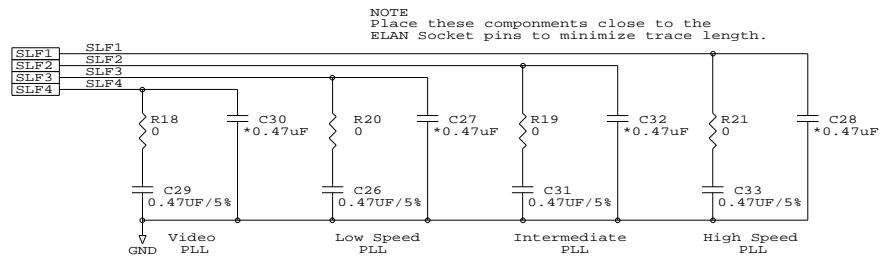




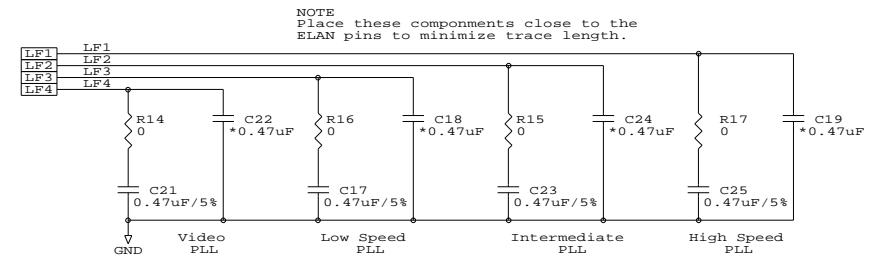




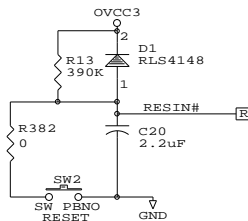
The 555 Timer is configured to function as a one-shot. ELAN revs B1 & B2 deliver a short RESDRV pulse when exiting uPower OFF mode which could cause some issues. That is the reason for this one-shot. The ELAN rev B3 device will address this issue & the one-shot is not needed.



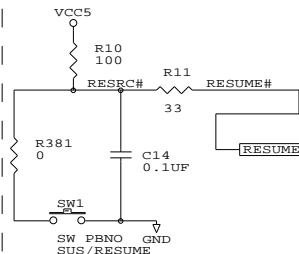
Elan Socket Loop Filters



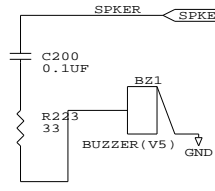
Elan Chip Loop Filters



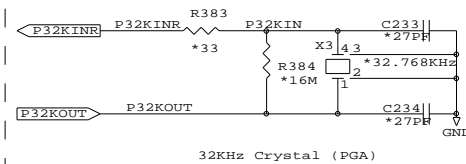
Reset Switch



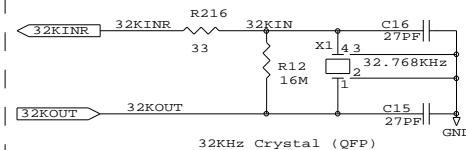
Suspend/Resume Switch



System Speaker

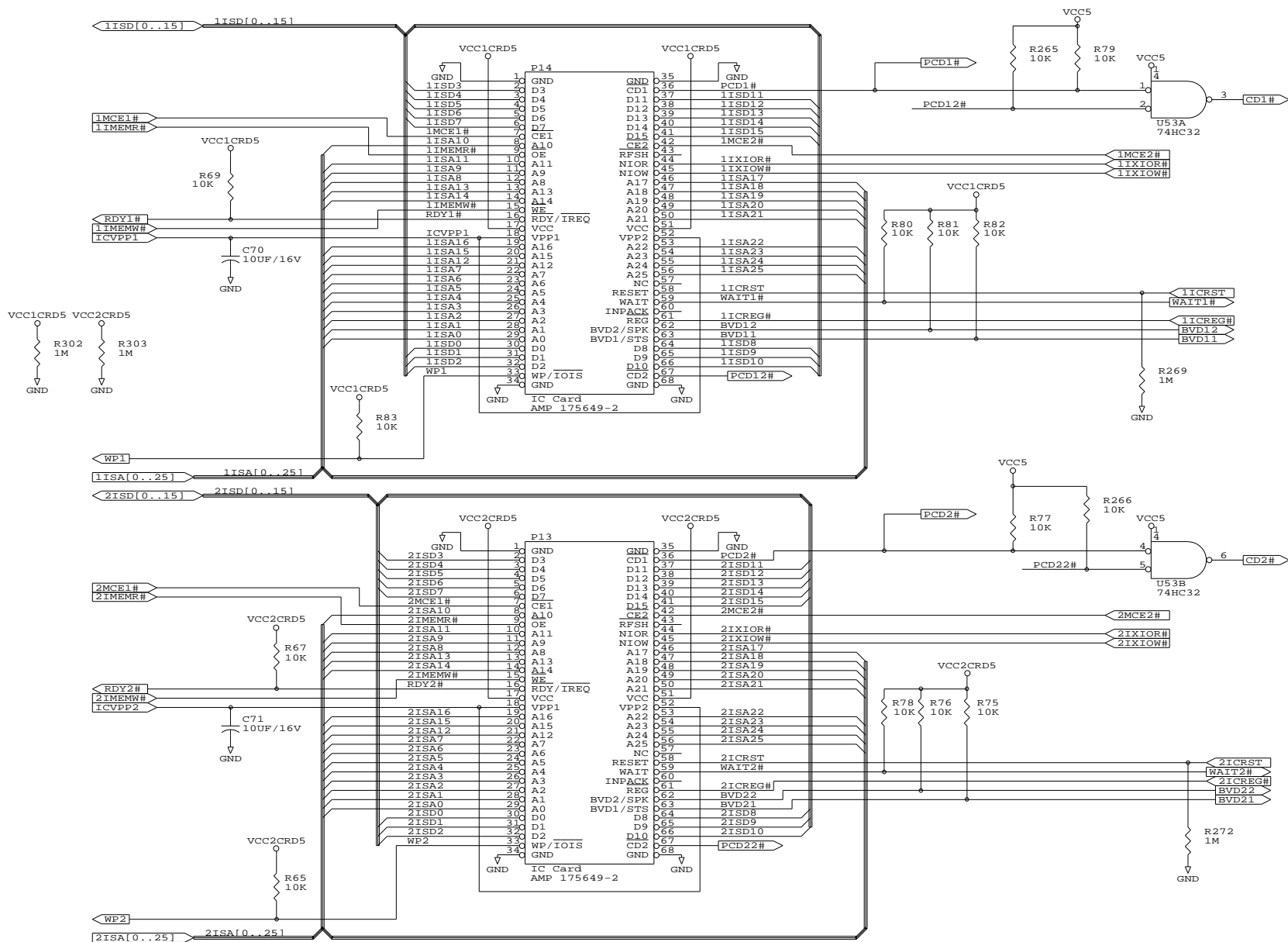


32KHz Crystal (PGA)

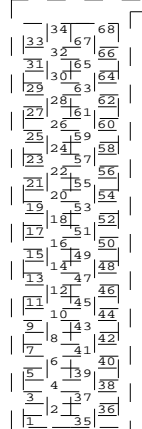


32KHz Crystal (QFP)

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Size	Document Number	REV
B	ELAN Evaluation Board	2.2
Date:	April 7, 1995	Sheet 4 of 23




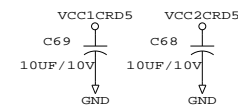
IC Card
Connector
AMP 175649-2

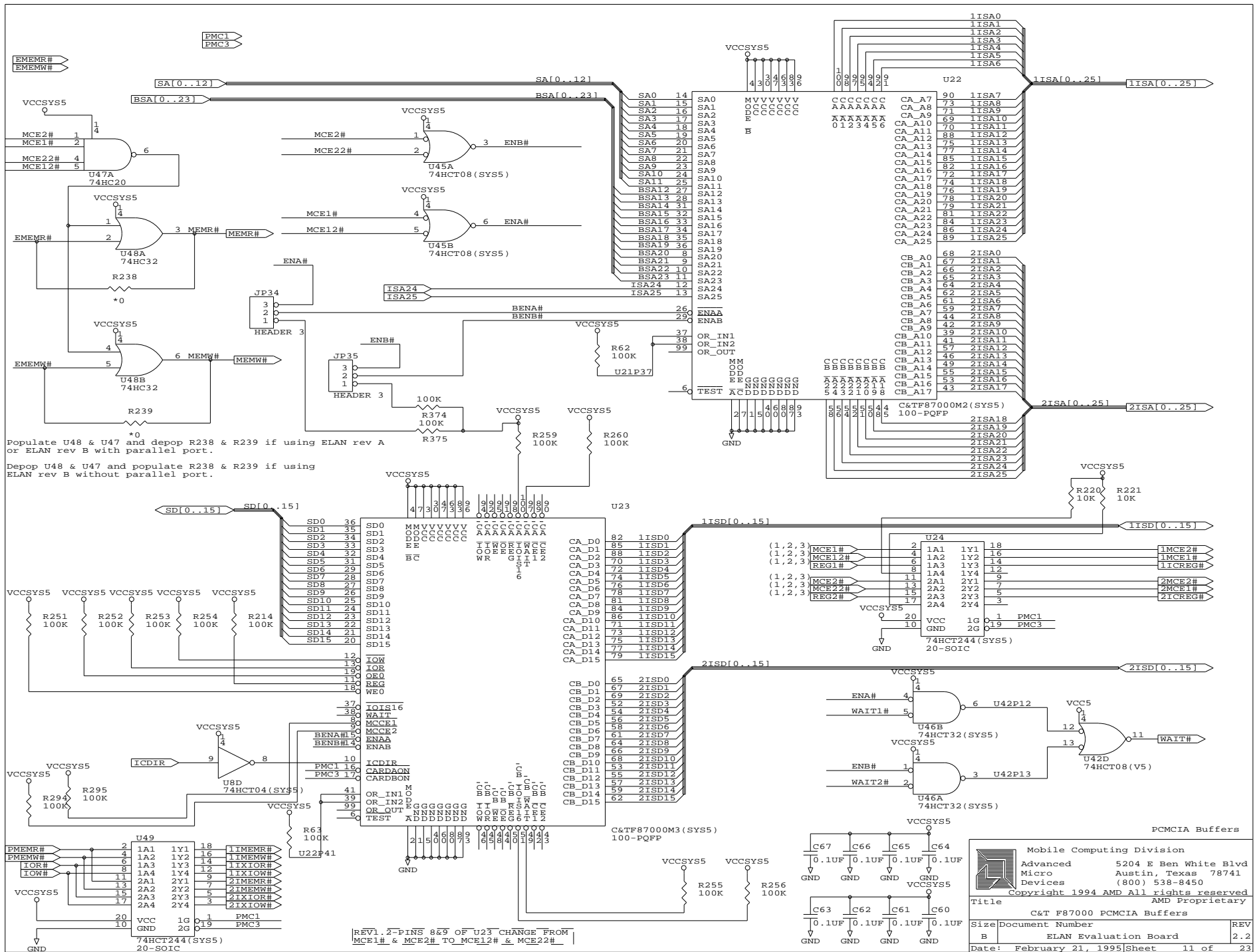


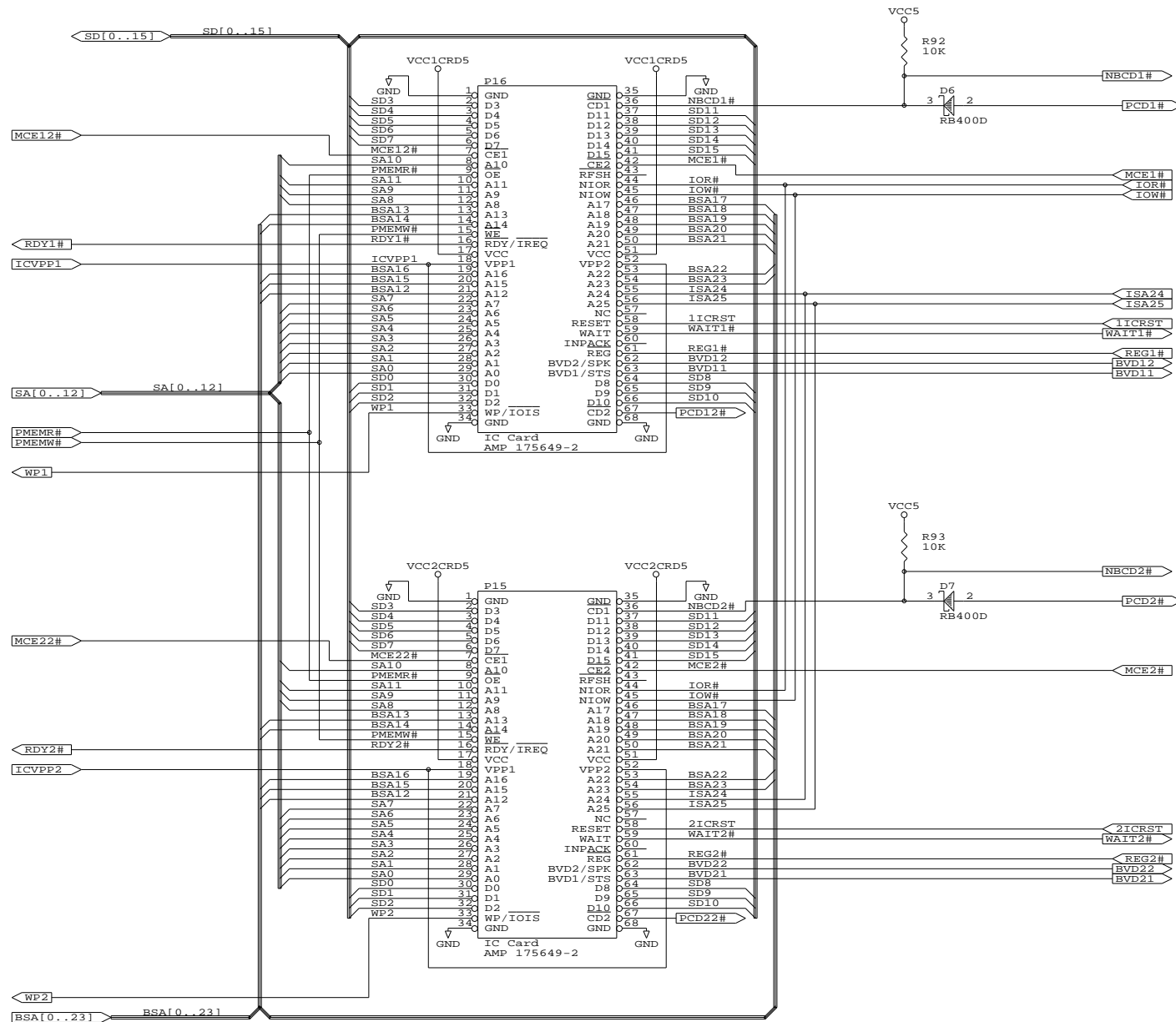
Component
side view

PCMCIA Buffered Connectors

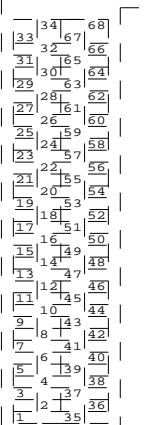
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Title Buffered PCMCIA Connectors			
Size/Document Number			REV
B	ELAN Evaluation Board		2.2
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IC Card
Connector
AMP 175649-2



Component
side view

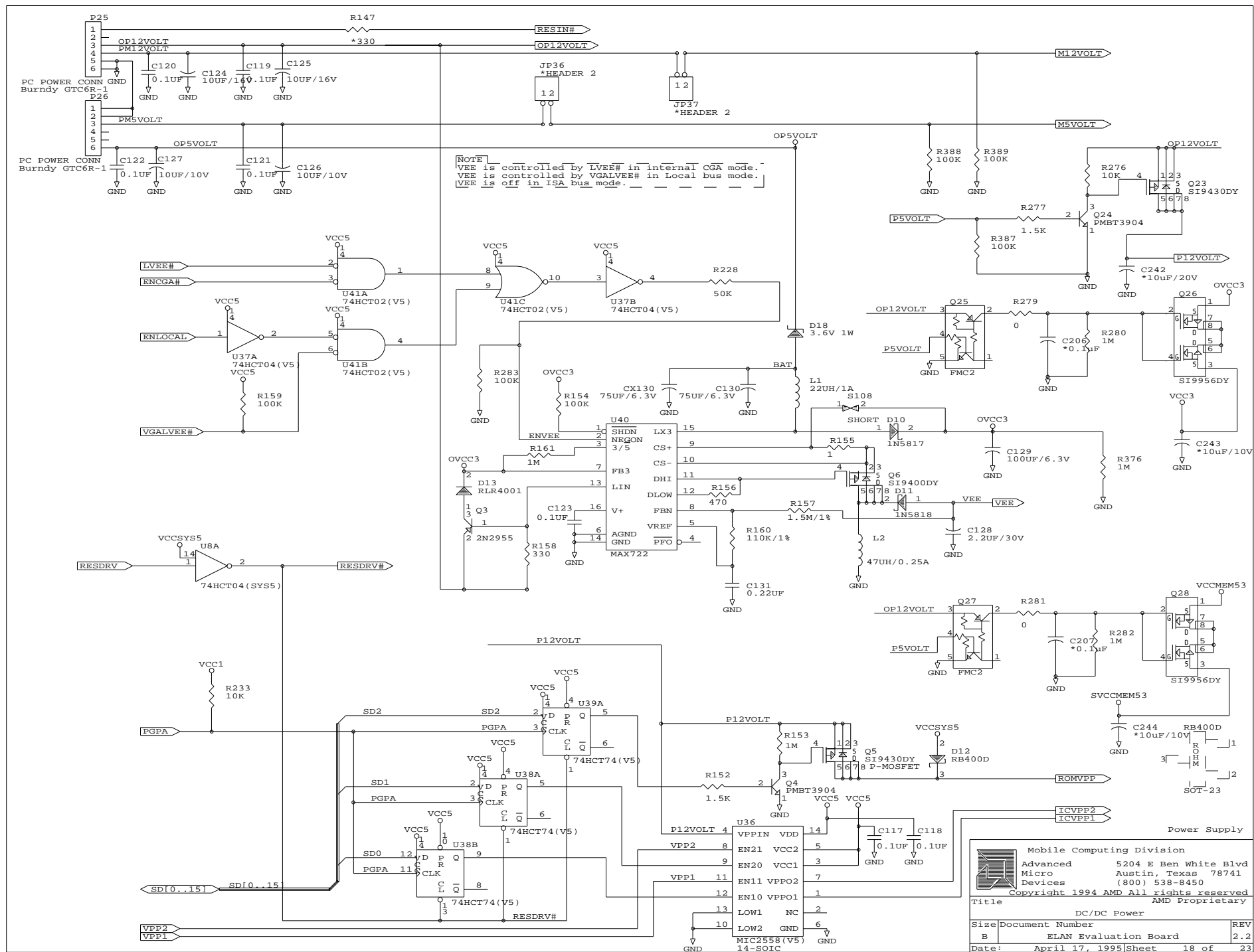
PCMCIA Non-Buffered Connectors

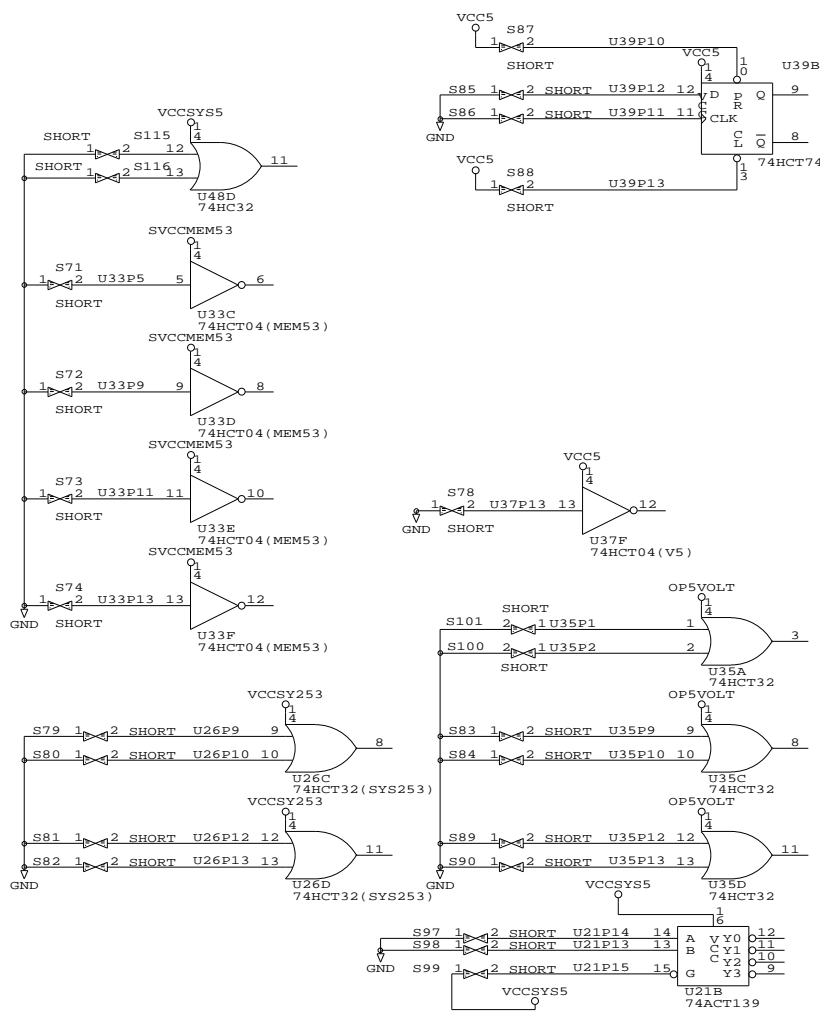


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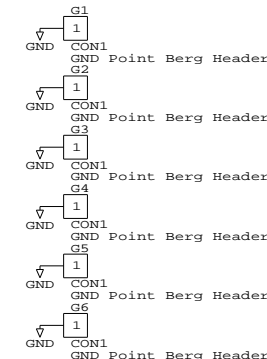
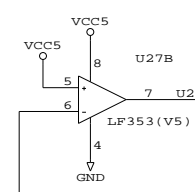
Title Non-Buffered PCMCIA Connectors		
Size/Document Number		
B	ELAN Evaluation Board	REV 2.2
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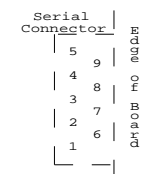
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2	2	2	2	2	2	2	2	2	2	2	2	2	2
3	3	3	3	3	3	3	3	3	3	3	3	3	3
4	4	4	4	4	4	4	4	4	4	4	4	4	4
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Grid of 10th Center Holes for Board Updates



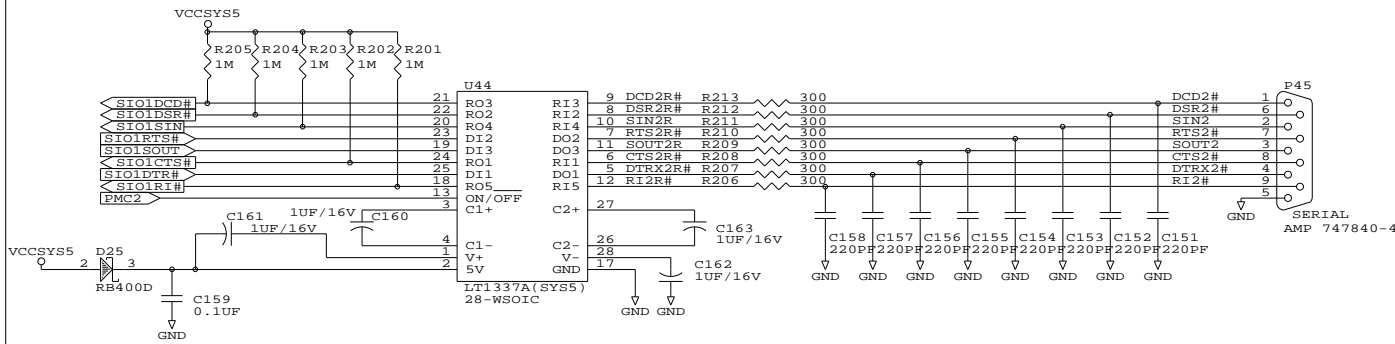
Spare Gates

Ground Posts for Debug



Component side view

Super I/O Serial Interface



Mobile Computing Division

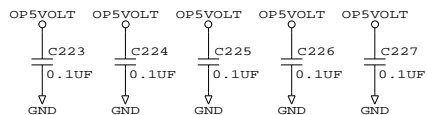
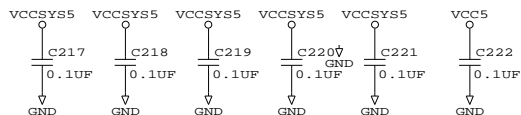
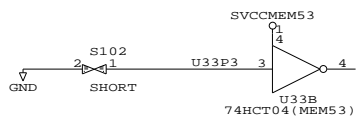
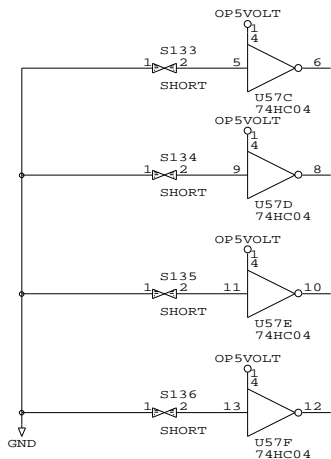
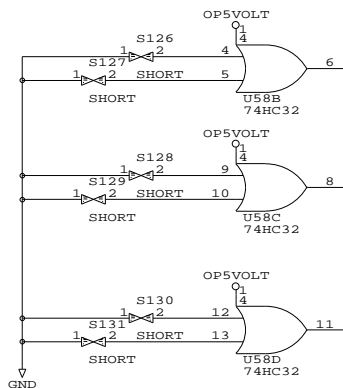
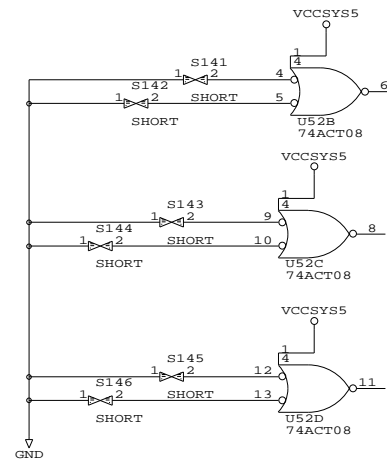
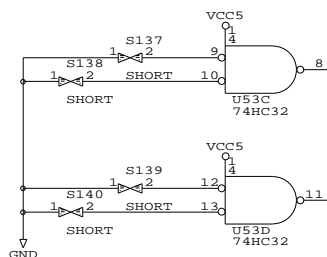
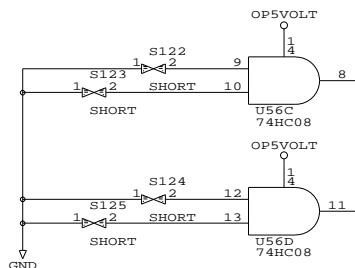
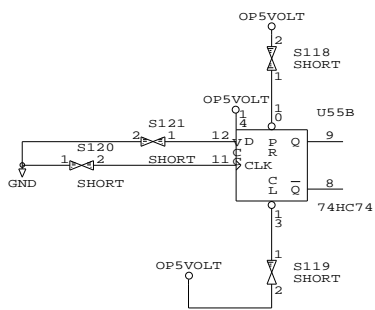
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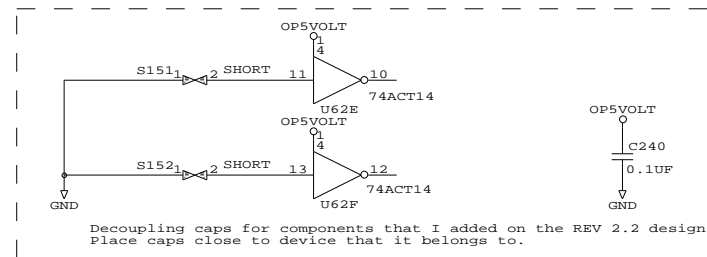
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Title	Spare Gates & Super I/O Serial Port	
Size/Document Number	B	ELAN Evaluation Board
REV	2.2	
Date: February 22, 1995	Sheet	22 of 23



Decoupling caps for components that I added on the REV 2.2 design.
Place caps close to device that it belongs to.



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Title SPARES continued		
Size	Document Number	REV
B	ELAN Evaluation Board	2.2
Date:	February 21, 1995	Sheet 23 of 23